CLAIMS

What is claimed is:

1. An integrated circuit, comprising:

at least one DC to DC converter for receiving a supply voltage and producing at least one intermediate voltage, at least one of said intermediate voltages having a greater voltage level than said supply voltage; and

processing circuitry for receiving at least one time-varying input signal and modifying a parameter of said time-varying signal to produce a modified time-varying signal.

2. The integrated circuit of claim 1, wherein said processing circuitry further receives said intermediate voltage having a greater voltage level than said supply voltage.

3. The integrated circuit of claim 1, wherein said parameter is selected from the group consisting of a voltage level and a frequency.

4. The integrated circuit of claim 1, wherein said modification is selected from the group consisting of increasing said parameter and decreasing said parameter.

5. The integrated circuit of claim 1, wherein said processing circuitry comprises digital circuitry.

6. The integrated circuit of claim 1, wherein said processing circuitry comprises analog circuitry.

7. The integrated circuit of claim 1, wherein said processing circuitry comprises analog and digital circuitry.

8. The integrated circuit of claim 1, wherein said time-varying input signal is a digital signal.

9. The integrated circuit of claim 1, wherein said time-varying input signal is an analog signal.

10. The integrated circuit of claim 1, wherein said parameter of said time-varying signal that is modified by said processing circuitry is programmable.

11. The integrated circuit of claim 1, wherein said processing circuitry comprises an input buffer and an output buffer.

12. The integrated circuit of claim 1, further comprising at least one passive element for providing programmability to said at least one intermediate voltage.

13. The integrated circuit of claim 12, wherein said at least one passive element is a peripheral passive element.

14. The integrated circuit of claim 1, wherein said DC to DC converter is switched capacitor based.

15. The integrated circuit of claim 1, wherein said integrated circuit further comprises a plurality of outputs, wherein an output voltage level a first of said outputs is greater than an output voltage level of a second of said outputs.

16. The integrated circuit of claim 1, wherein said output voltage of said first output is a DC voltage greater than said supply voltage.

17. A circuit board, comprising:

a plurality of integrated circuits disposed on said board, said plurality of integrated circuits requiring a plurality of voltage levels and signals for operation; and

an integrated power supply circuit disposed on said board, said integrated power supply circuit comprising:

at least one DC to DC converter for receiving a supply voltage and producing at least one intermediate voltage, at least one of said intermediate voltages having a greater voltage level than said supply voltage;

processing circuitry for receiving at least one time-varying input signal and modifying a parameter of said time-varying signal to produce a modified time-varying signal; and

a plurality of outputs, wherein an output voltage level a first of said outputs is greater than an output voltage level of a second of said outputs.

18. The circuit board of claim 17, wherein said processing circuitry further receives said intermediate voltage having a greater voltage level than said supply voltage.

19. The circuit board of claim 17, wherein said parameter is selected from the group consisting of a voltage level and a frequency.

20. The circuit board of claim 17, wherein said modification is selected from the group consisting of increasing said parameter and decreasing said parameter.

21. The circuit board of claim 17, wherein said processing circuitry comprises digital circuitry.

22. The circuit board of claim 17, wherein said processing circuitry comprises analog circuitry.

23. The circuit board of claim 17, wherein said processing circuitry comprises analog and digital circuitry.

24. The circuit board of claim 17, wherein said time-varying input signal is a digital signal.

25. The circuit board of claim 17, wherein said time-varying input signal is an analog signal.

26. The circuit board of claim 17, wherein said parameter of said time-varying signal that is modified by said processing circuitry is programmable.

27. The circuit board of claim 17, wherein said processing circuitry comprises an input buffer and an output buffer.

28. The circuit board of claim 17, further comprising at least one passive element for providing programmability to said at least one intermediate voltage.

29. The circuit board of claim 28, wherein said at least one passive element is a peripheral passive element.

30. The circuit board of claim 17, wherein said DC to DC converter is switched capacitor based.

31. The circuit board of claim 17, wherein said output voltage of said first output is a DC voltage greater than said supply voltage.